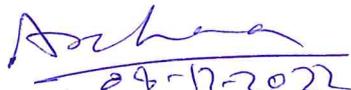


Government of India
Bhabha Atomic Research Center
Pulsed Power & Electromagnetics Division
Reference: BARCV/PP&EMD/2022-23/R&D-71/MF/SKS/15; dt: 7/12/2022

Name of Party:

Dear Sir,

1. Quotations are invited for the minor fabrication job, as per the enclosed specification
2. Bidder shall quotes for fabrication of these components with materials.
3. Taxes shall be quoted separately.
4. The quotation must reach the Head, Pulsed Power & ElectroMagnetics Division by date 21.12.2022 and must be sent in a sealed, printed envelope superscripted with reference number and the due date given above. Quotation opening time is 4:00 PM. The party may send its representative at the time of opening.
5. The address of the envelop should read –
Dr. S K Sharma C/o Dr. Archana Sharma
PP&EMD, Bhabha Atomic Research Centre
Gajuwaka – Yellamanchil Road, Near Nagavaram Jn,
Maduthuru Sub PO, Visakhapatnam - 531011
6. The fabrication work shall be subjected to inspection by our Engineer. The finished component shall not be dispatched prior to approval of our engineer at bidders premises. Necessary inspection facilities should be provided to our engineer during fabrication at bidder's premises.
7. The bidder shall deliver the finished components after approval by our engineer with in 3 months from the date of firm work order issued to the bidder. The finished components along with left over material shall be delivered by the bidder at the address mention in point no 5
8. Head, PP&EMD reserves the rights to accept/ reject any or all quotations without assigning any reasons.
9. Delivery charges if any, must be clearly mentioned with the offer,
10. Quotation must also indicate the validity of offer.
11. Drawing / sketches must be returned along with the offer.
12. Quotations are to be in printed letter-head / quotation format only. Quotation received in computer-generated forms will be considered as invalid and rejected.
13. Quotation should consists of Sales Tax registration number (Registered with local ST / CST authority), PAN number of the firm, services tax registration number etc.
14. Claim preferred by the firms are also be in printed INVOICE format consisting of the above registration numbers.
15. No Free issue material will be given.


24-12-2022
(Dr. Archana Sharma)
Director, BTDG
BARC, Mumbai

डॉ. (श्रीमती) अर्चना शर्मा / ARCHANA SHARMA
निदेशक / Director
किरणपुंज प्रौद्योगिकी विकास वर्ग
Beam Technology Development Group
भारत सरकार / Government of India,
भा.प.अ. केंद्र / B. A. R.C.
टॉम्बो, मुंबई / Trombay, Mumbai - 400 085.

Item: Fabrication and supply of high voltage vacuum feed through and high voltage protection circuit for plasma device

Quantity: 1 Number

Detailed Specifications

1. Introduction

High voltage vacuum feed through is used to provide high voltage up to 80 kV to grid inside vacuum chamber. Protection circuit is provided to plasma discharges to limit the current of high voltage power supply. It consists of capacitor, resistors and high voltage diodes. The schematic diagram of protection circuit is shown in Fig 1.

2. Specification of high voltage vacuum feed through

Qty: 1 Number

- Conflat Flange : 6 ” NW100CF
- Housing & Conductor : 304 Stainles Steel
- Insulation : Alumina Ceramic
- Voltage Rating : 80 kV
- Current rating : 6.5 A
- Temperature range : 10 deg C – 450 deg C

3. Specification of high voltage protection circuit.

Qty: 1 Number

- Capacitor : 80 kV / 20 nF (5 Numbers)
- Resistance : 1kOhm (3 Numbers)
- High Voltage Diode : 100 kV/ 20 mA (8 Numbers)

The high voltage protection circuit should be enclosed in closed chamber.

4. Stage wise execution of the work

- (a) Approval of items
- (b) Fabrication of protection circuit
- (c) Testing of high voltage protection circuit

Note:

- 1. Material has to be delivered to BARC Visakhapatnam
- 2. Delivery period to supply the material is 3 Months. No delivery date extension will be provided under any circumstances.
- 3. Party has to quote for complete set of item. No splitting of order will be done.

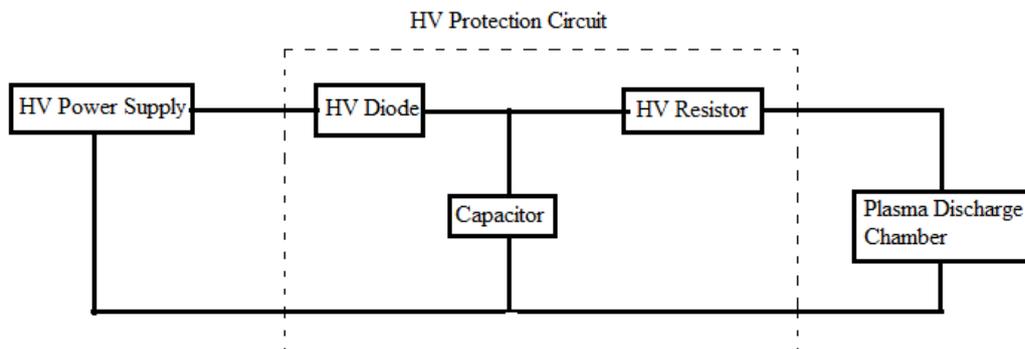


Fig 1. Schematic of Protection circuit